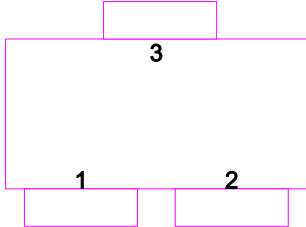


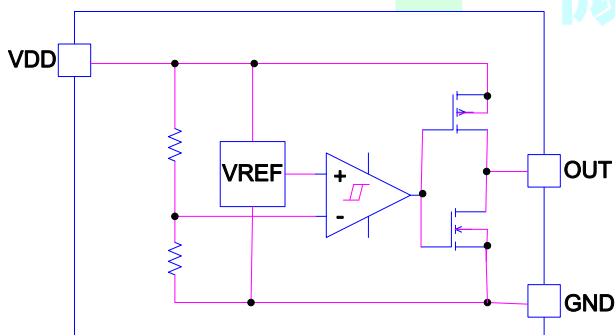
Pin Assignment

Package Type	Pin Configurations
SOT23	

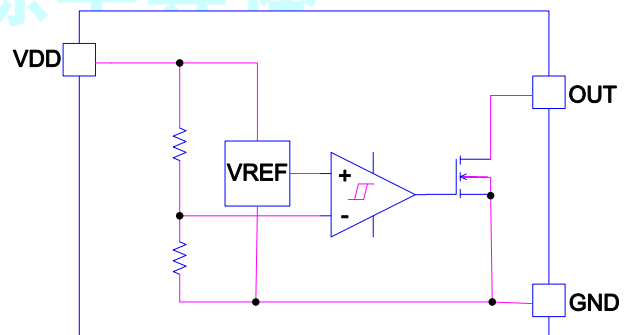
Pin Description

Pin Number	Pin Name	Function
1	VOUT	Voltage Detection output Pin.
2	VSS	GND pin.
3	VDD	Voltage input pin.

Block Diagram



CMOS Output Type



N-channel Output Open Drain

Absolute Maximum Ratings

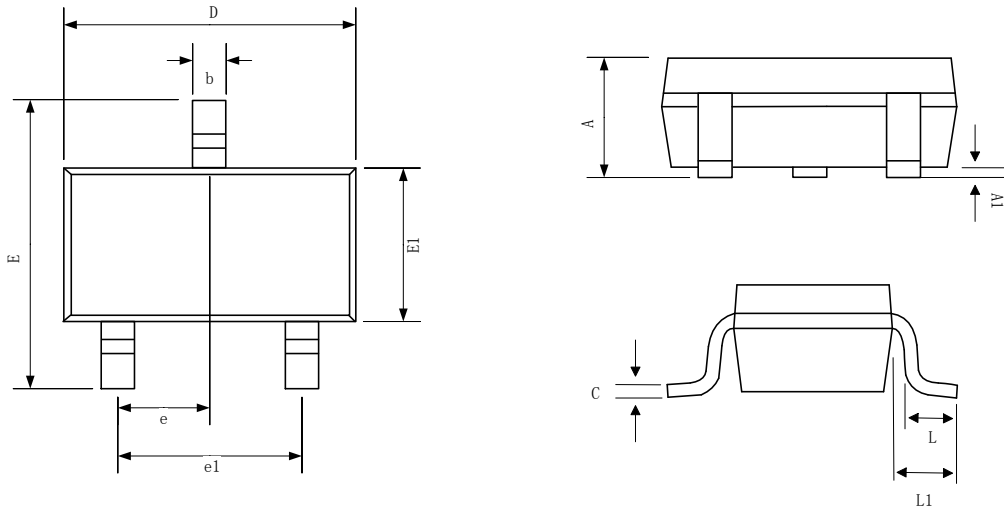
◇ Input Voltage range	-----	-0.3V~6V
◇ Output Voltage range	-----	-0.3V~6V
◇ Maximum Output current	-----	70mA
◇ Maximum power dissipation	-----	150mW
◇ Ambient temperature	-----	-40~+85 C
◇ Storage temperature	-----	-40~125 C
◇ Lead temperature and time	-----	260°C,10S

Electrical Characteristics

(LP3808-263B3F, T=25°C, Unless otherwise specified.)

Symbol	Parameter	Conditions	Reference data			Unit
			Min.	Typ.	Max.	
VDET	Detector Threshold		2.58	2.63	2.72	V
VHYS	Detector Threshold Hysteresis		0.054	0.108	0.162	V
ISS	Current consumption			5		uA
VDDH	Maximum operating voltage				5.5	V
VDDL	Minimum Operating voltage			2.5		V
IOUT	Output current	Nch VDS=0.05V VDD=0.70V	0.01	0.05		mA
		Pch VDS=-2.1V VDD=4.50V	1.0	2.0		mA
TDELAY	Output Delay Time			200		mS

Packaging Information



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.900		1.200
A1	0.000	0.050	0.100
b	0.300	0.400	0.500
c	0.008	0.120	0.150
D	2.800	2.900	3.000
E	2.250	2.400	2.550
E1	1.200	1.300	1.400
e	0.950BSC		
e1	1.900BSC		
L	0.200	0.350	0.500
L1	0.550REF		